
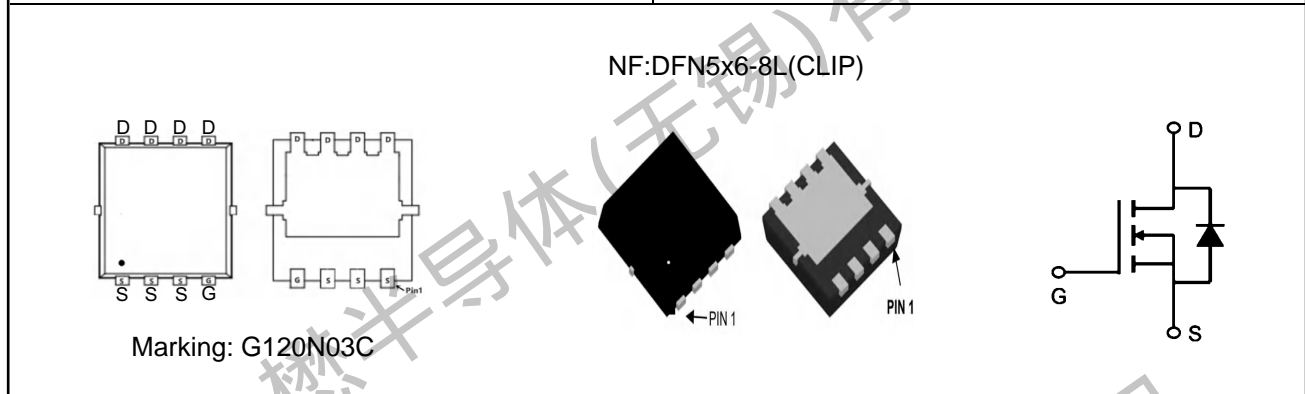


TMG120N03CNF

N-Channel Enhancement Mosfet

<p>General Description</p> <ul style="list-style-type: none"> • Low $R_{DS(ON)}$ • RoHS and Halogen-Free Compliant <p>Applications</p> <ul style="list-style-type: none"> • Load switch • PWM 	<p>General Features</p> <p>$V_{DS} = 30V$ $I_D = 120A$</p> <p>$R_{DS(ON)} = 2.0m\Omega(\text{typ.})@V_{GS} = 10V$</p> <p>100% UIS Tested 100% R_g Tested</p> 
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Absolute Maximum Ratings ($T_c = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_c=25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V$	120	A
$I_D@T_c=100^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V$	75	A
I_{DM}	Pulsed Drain Current ¹	380	A
EAS	Single Pulse Avalanche Energy ²	101	mJ
$P_D@T_c=25^\circ\text{C}$	Total Power Dissipation	69	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ³	---	62	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case	---	3.6	$^\circ\text{C}/\text{W}$

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Electrical Characteristics (T_J = 25°C, unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit	
Static Characteristics							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	30	-	-	V	
Gate-body Leakage Current	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30V, V _{GS} = 0V	T _J =25°C	-	-	1	μA
			T _J =125°C	-	2	-	
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	0.9	1.2	1.5	V	
Drain-Source On-Resistance ⁴	R _{DSON}	V _{GS} = 10V, I _D = 22.5A	-	2.0	2.4	mΩ	
		V _{GS} = 4.5V, I _D = 22.5A	-	3.0	3.8		
Forward Transconductance ⁴	g _{fs}	V _{DS} = 10V, I _D = 20A	-	130	-	S	
Dynamic Characteristics⁵							
Input Capacitance	C _{iss}	V _{DS} = 15V, V _{GS} = 0V, f = 1MHz	-	1900	-	pF	
Output Capacitance	C _{oss}		-	1419	-		
Reverse Transfer Capacitance	C _{rss}		-	111	-		
Gate Resistance	R _g	f = 1MHz	-	2.5	-	Ω	
Switching Characteristics⁵							
Total Gate Charge	Q _g	V _{GS} = 10V, V _{DS} = 15V, I _D = 20A	-	39	-	nC	
Gate-Source Charge	Q _{gs}		-	11	-		
Gate-Drain Charge	Q _{gd}		-	4.5	-		
Turn-On Delay Time	t _{d(on)}	V _{GS} = 10V, V _{DD} = 20V, R _G = 3Ω, I _D = 20A	-	10	-	ns	
Rise Time	t _r		-	37	-		
Turn-Off Delay Time	t _{d(off)}		-	45	-		
Fall Time	t _f		-	16	-		
Body Diode Reverse Recovery Time	t _{rr}	I _F = 20A, dI/dt = 100A/μs	-	49	-	ns	
Body Diode Reverse Recovery Charge	Q _{rr}		-	35	-	nC	
Drain-Source Body Diode Characteristics							
Diode Forward Voltage ⁴	V _{SD}	I _S = 45, V _{GS} = 0V	-	-	1.4	V	
Continuous Source Current	I _S	T _C = 25°C	-	-	120	A	

Notes:

1. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)} = 150°C.
2. The EAS data shows Max. rating. The test condition is V_{DD} = 24V, V_{GS} = 10V, L = 0.1mH, R_G = 25Ω
3. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper. The value in any given application depends on the user's specific board design.
4. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
5. This value is guaranteed by design hence it is not included in the production test.

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Typical Characteristics

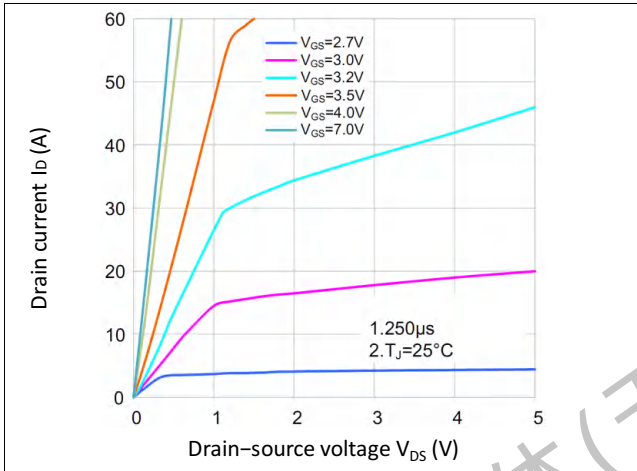


Figure 1. Output Characteristics

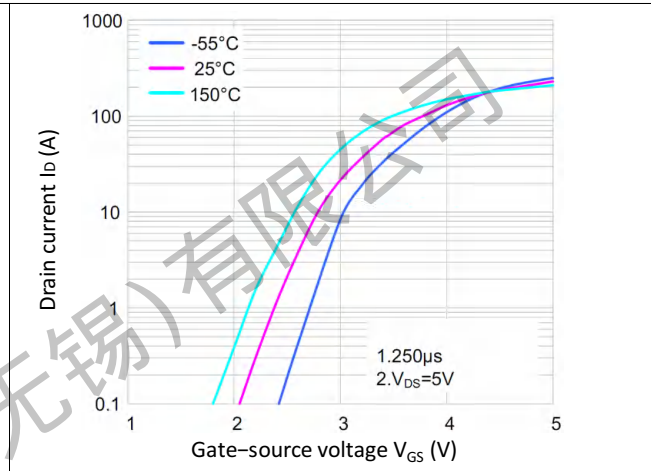


Figure 2. Transfer Characteristics

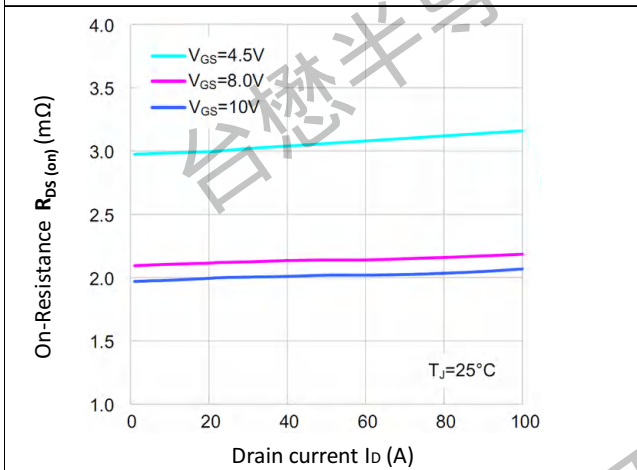


Figure 3. $R_{DS(ON)}$ vs. I_D

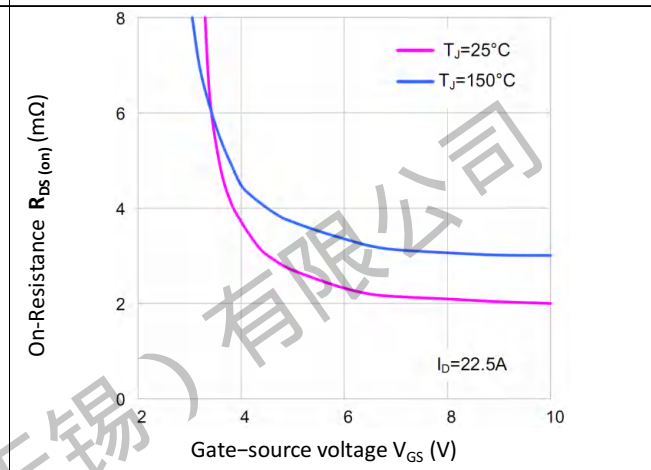


Figure 4. $R_{DS(ON)}$ vs. V_{GS}

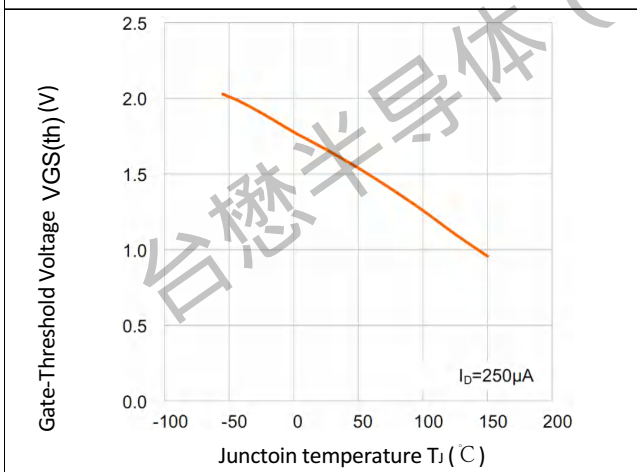


Figure 5. $V_{GS(th)}$ vs. T_J

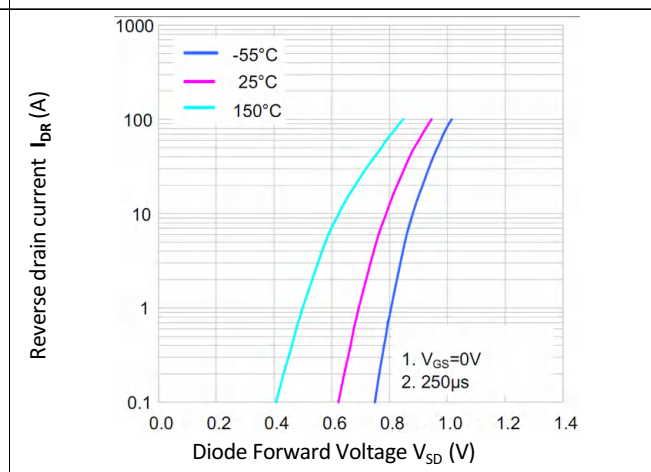


Figure 6. I_{DR} vs. V_{SD}

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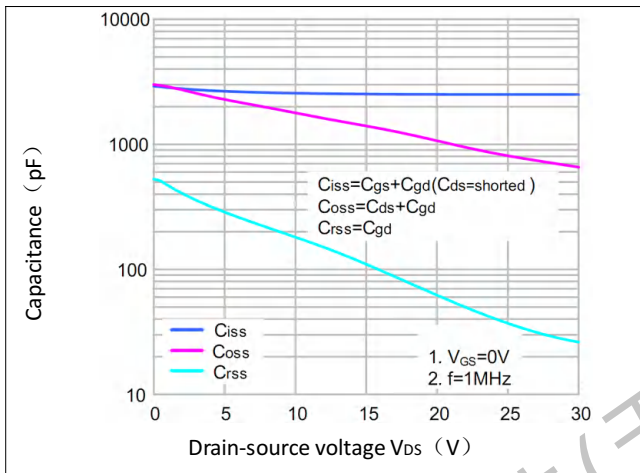


Figure 7. Capacitance Characteristics

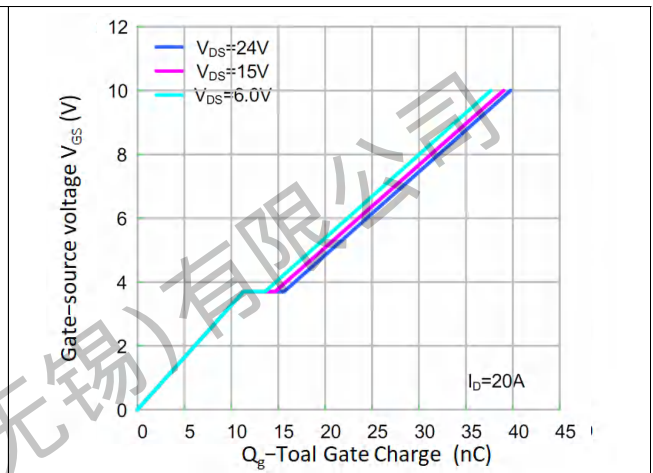


Figure 8 Gate Charge Characteristics

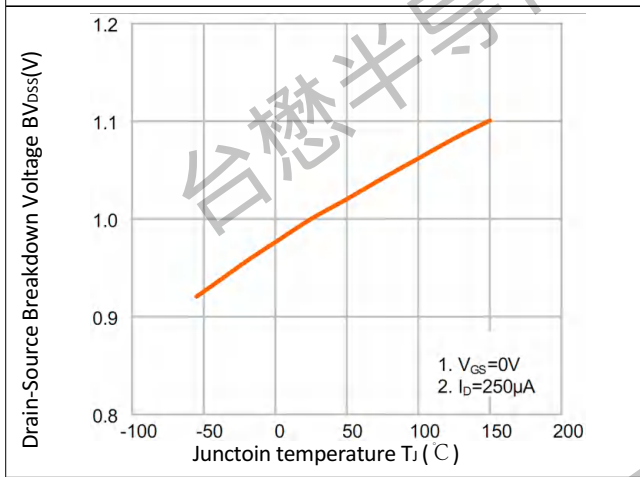


Figure 9. BVdss VS TJ

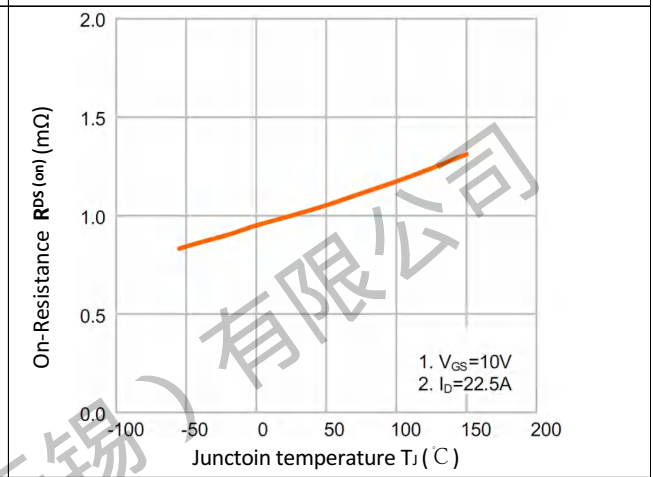


Figure 10. RDS(ON) VS TJ

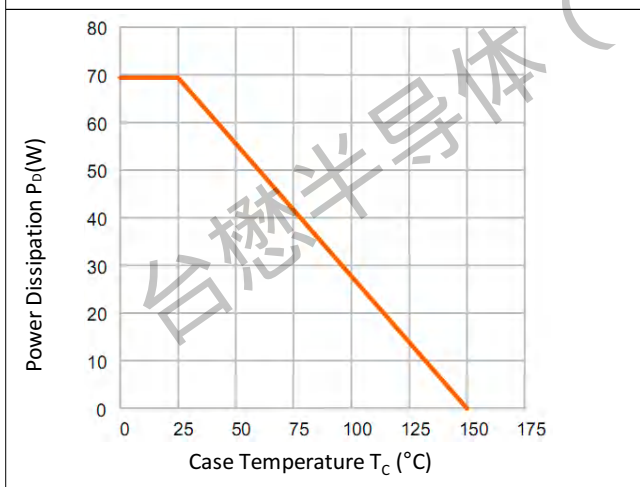


Figure 11. Power Dissipation

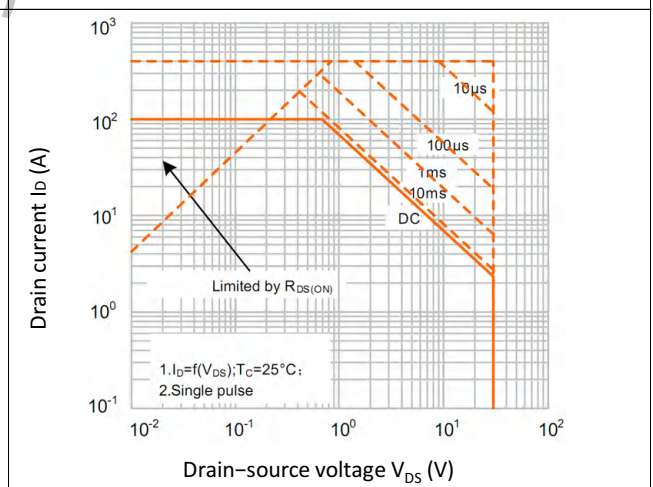


Figure 12. Safe Operating Area

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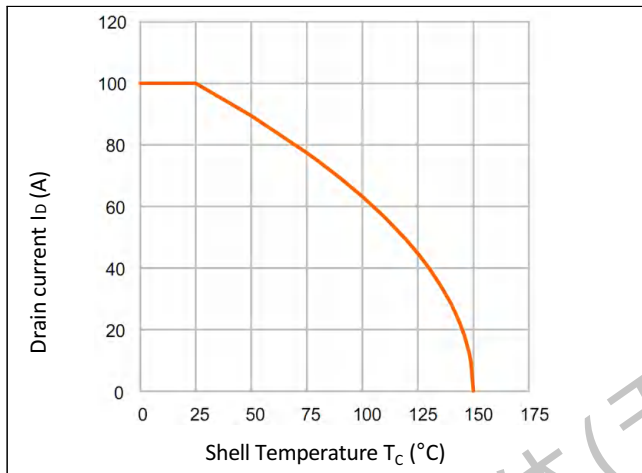


Figure 13. Drain current VS Shell Temperature

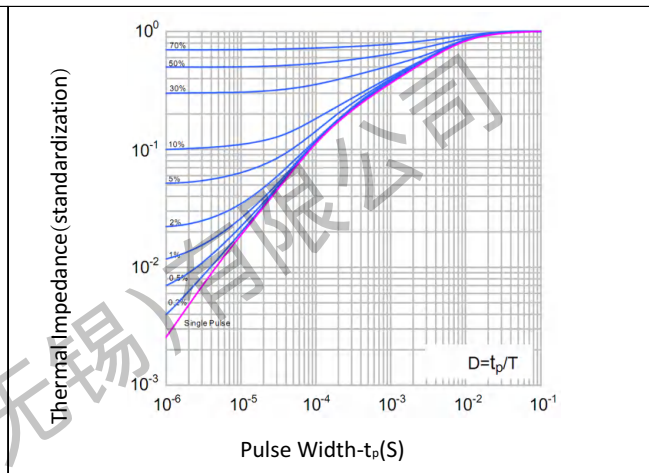
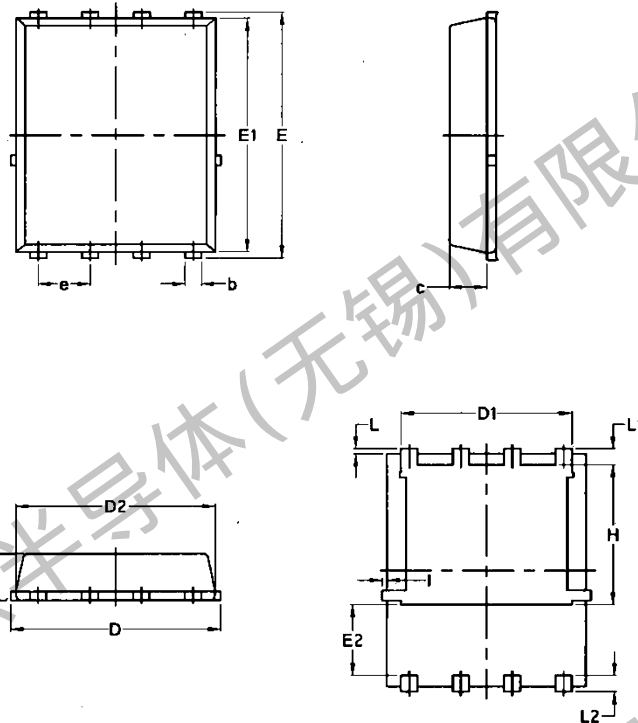


Figure 14 Transient thermal impedance VS Pulse Width

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Package Mechanical Data:DFN5x6-8L(CLIP)

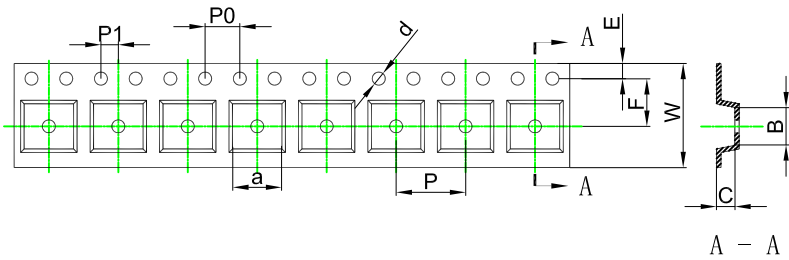


Symbol	Common			
	mm		Inch	
	Min	Max	Min	Max
A	1.03	1.17	0.0406	0.0461
b	0.34	0.48	0.0134	0.0189
c	0.824	0.0970	0.0324	0.082
D	4.80	5.40	0.1890	0.2126
D1	4.11	4.31	0.1618	0.1697
D2	4.80	5.00	0.1890	0.1969
E	5.95	6.15	0.2343	0.2421
E1	5.65	5.85	0.2224	0.2303
E2	1.60	/	0.0630	/
e	1.27 BSC		0.05 BSC	
L	0.05	0.25	0.0020	0.0098
L1	0.38	0.50	0.0150	0.0197
L2	0.38	0.50	0.0150	0.0197
H	3.30	3.50	0.1299	0.1378
l	/	0.18	/	0.0070

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PDFN5x6-8L Embossed Carrier Tape



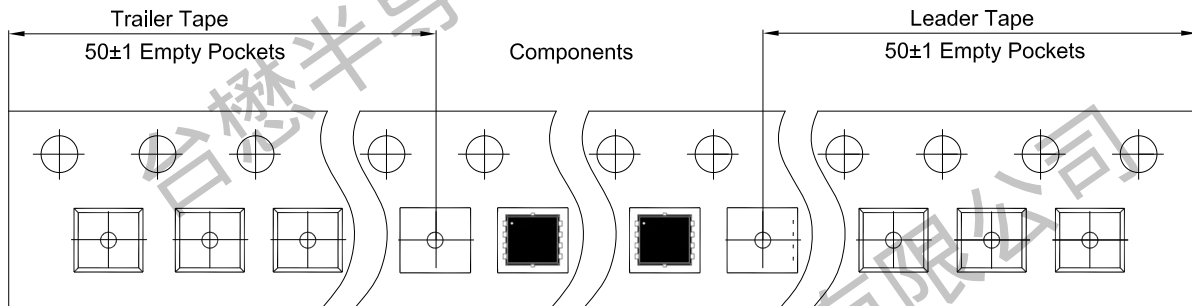
Packaging Description:

SOP-8L parts are shipped in tape. The carrier tape is made from a dissipative (carbon filled) polycarbonate resin. The cover tape is a multilayer film (Heat Activated Adhesive in nature) primarily composed of polyester film, adhesive layer, sealant, and anti-static sprayed agent. These reeled parts in standard option are shipped with 2,500 units per 13" or 33cm diameter reel. The reels are clear in color and is made of polystyrene plastic (anti-static coated).

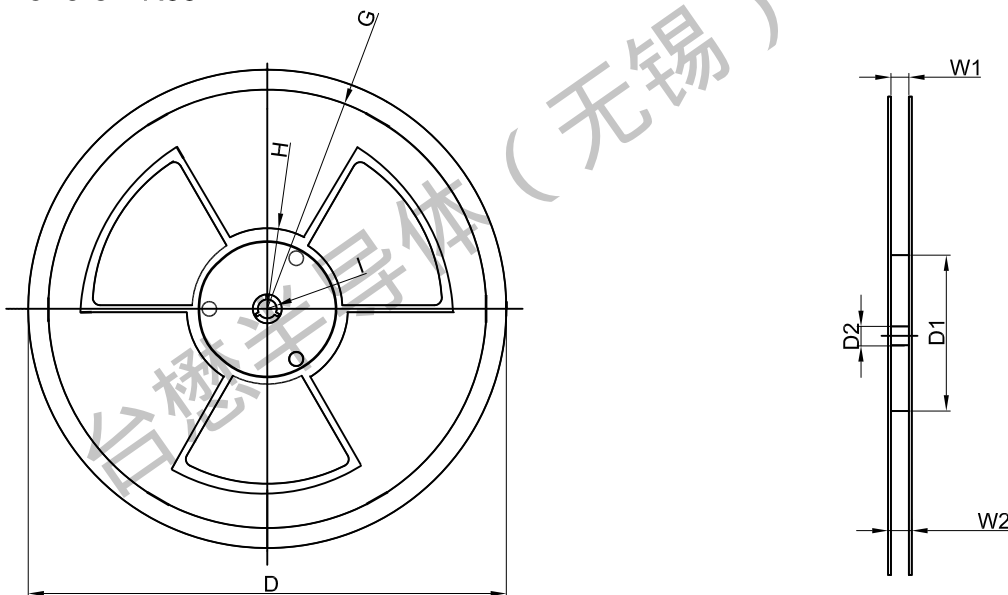
ALL DIM IN mm

Dimensions are in millimeter										
Pkg type	a	B	C	d	E	F	P0	P	P1	W
PDFN5x6-8L	6.40	5.40	2.10	Ø1.50	1.75	5.50	4.00	8.00	2.00	12.00

PDFN5x6-8L Tape Leader and Trailer



PDFN5x6-8L Reel



Dimensions are in millimeter								
Reel Option	D	D1	D2	G	H	I	W1	W2
13"Dia	Ø330.00	100.00	13.00	R135.00	R55.00	R6.50	12.00	14.00

REEL	Reel Size	Box	Box Size(mm)	Carton	Carton Size(mm)	G.W.(kg)
5,000 pcs	13 inch	10,000 pcs	370×355×52	50,000 pcs	400×360×368	

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Revision history:

Date	Rev	Description	Page
2023.08.16	23.08	Original	