

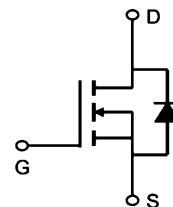
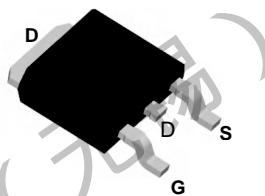
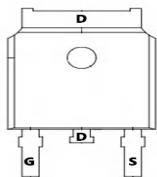
TM80N04D

N-Channel Enhancement Mosfet

General Description <ul style="list-style-type: none"> • Low R_{DS(ON)} • RoHS and Halogen-Free Compliant Applications <ul style="list-style-type: none"> • Load switch • PWM 	General Features <p>V_{DS} = 40V I_D = 80A R_{DS(ON)}= 5.6mΩ(typ.) @ V_{GS}= 10V</p> <p>100% UIS Tested 100% R_g Tested</p>
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D:TO-252-3L



Marking: 80N04

Absolute Maximum Ratings (T_c = 25°C Unless Otherwise Noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	40	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _c =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	80	A
I _D @T _c =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	52	A
I _{DM}	Pulsed Drain Current ²	296	A
EAS	Single Pulse Avalanche Energy ³	416.1	mJ
I _{AS}	Avalanche Current	39	A
P _D @T _c =25°C	Total Power Dissipation ³	62.6	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient ¹	---	62	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	6.6	°C/W

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Electrical Characteristics ($T_c=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	40	45	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=40\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	± 100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.2	1.6	2.0	V
Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=20\text{A}$	-	5.6	7.9	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=20\text{A}$		7.8	11	
Forward Transconductance	g_{FS}	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=20\text{A}$	15	-	-	S
Dynamic Characteristics (Note 4)						
Input Capacitance	C_{iss}	$V_{\text{DS}}=20\text{V}, V_{\text{GS}}=0\text{V}, F=1.0\text{MHz}$	-	1780	-	PF
Output Capacitance	C_{oss}		-	280	-	PF
Reverse Transfer Capacitance	C_{rss}		-	190	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=20\text{V}, I_{\text{D}}=2\text{A}, R_{\text{L}}=1\Omega$ $V_{\text{GS}}=10\text{V}, R_{\text{G}}=3\Omega$	-	6.4	-	nS
Turn-on Rise Time	t_r		-	17.2	-	nS
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	29.6	-	nS
Turn-Off Fall Time	t_f		-	16.8	-	nS
Total Gate Charge	Q_g	$V_{\text{DS}}=20\text{V}, I_{\text{D}}=20\text{A}, V_{\text{GS}}=10\text{V}$	-	29	-	nC
Gate-Source Charge	Q_{gs}		-	4.5	-	nC
Gate-Drain Charge	Q_{gd}		-	6.4	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=10\text{A}$	-		1.2	V
Diode Forward Current (Note 2)	I_{S}		-	-	80	A
Reverse Recovery Time	t_{rr}	$T_{\text{J}} = 25^\circ\text{C}, \text{IF} = 20\text{A}$ $dI/dt = 100\text{A}/\mu\text{s}$ (Note 3)	-	29	-	nS
Reverse Recovery Charge	Q_{rr}		-	26	-	nC
Forward Turn-On Time	t_{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production
5. E_{AS} condition : $T_j=25^\circ\text{C}, V_{\text{DD}}=20\text{V}, V_{\text{G}}=10\text{V}, L=1\text{mH}, R_{\text{G}}=25\Omega$,

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Typical Electrical and Thermal Characteristics (Curves)

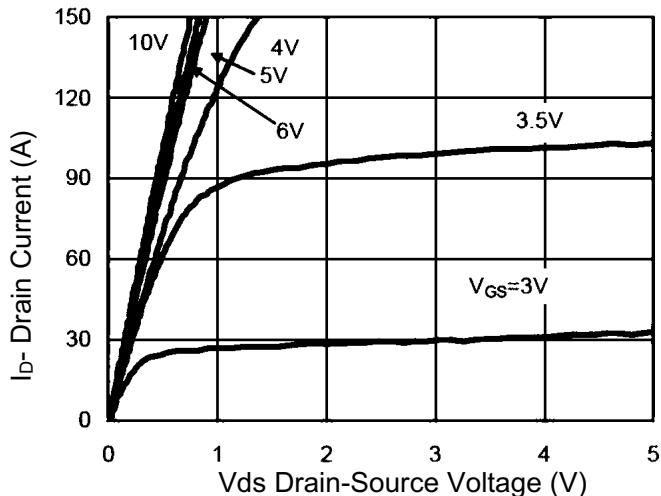


Figure 1 Output Characteristics

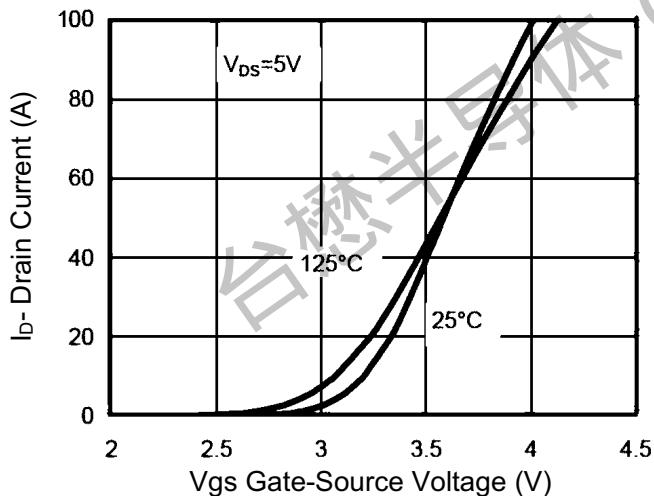


Figure 2 Transfer Characteristics

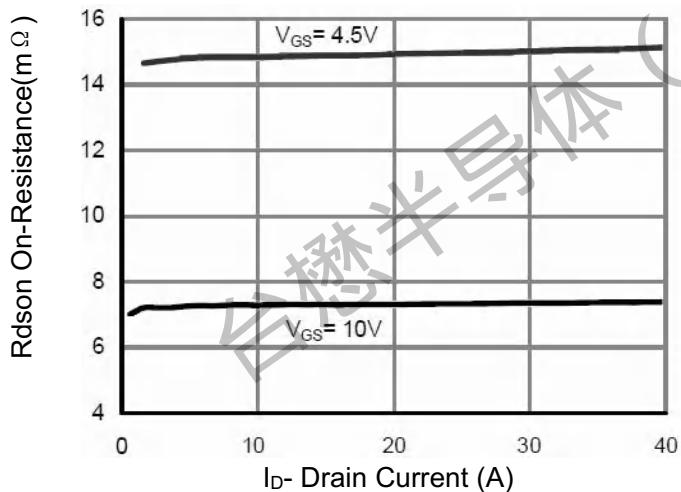


Figure 3 Rdson- Drain Current

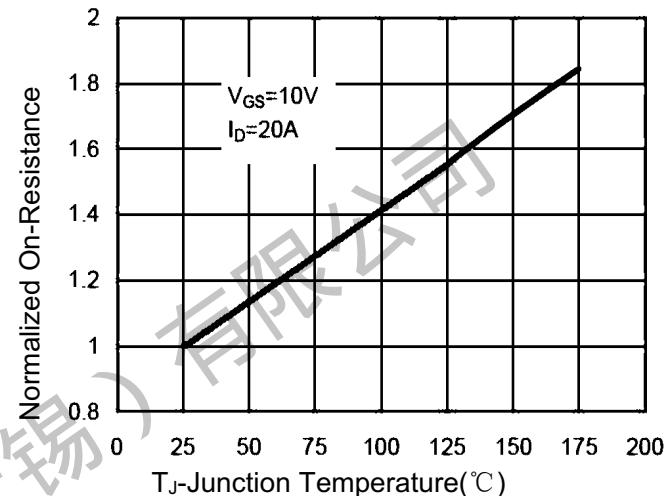


Figure 4 Rdson-JunctionTemperature

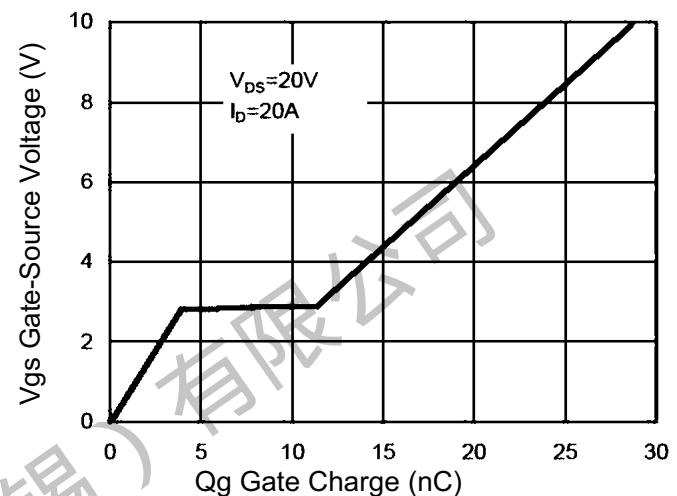


Figure 5 Gate Charge

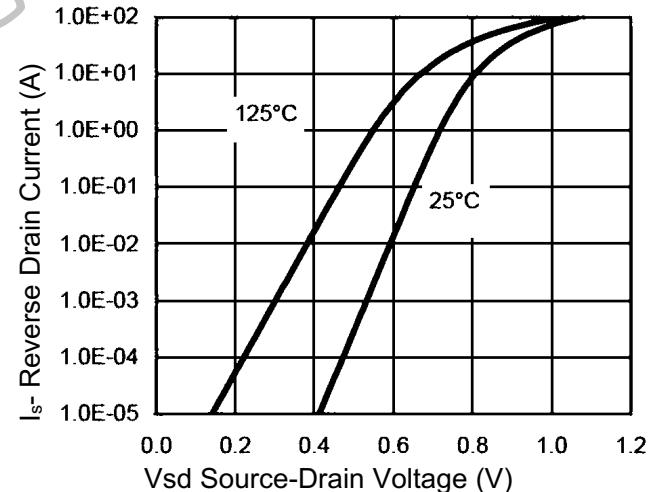


Figure 6 Source- Drain Diode Forward

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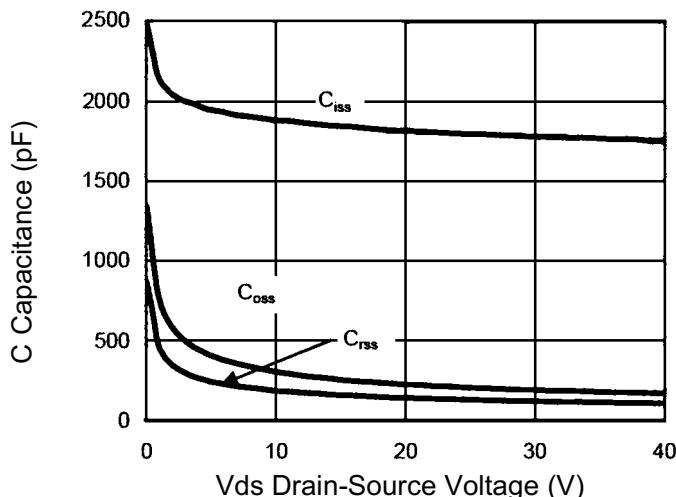


Figure 7 Capacitance vs Vds

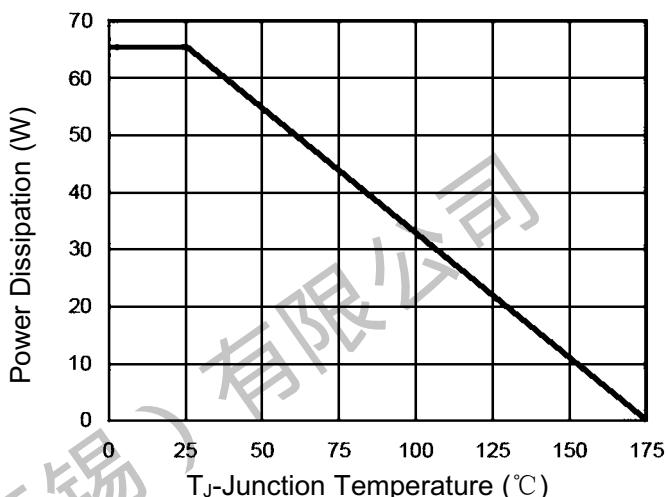


Figure 9 Power De-rating

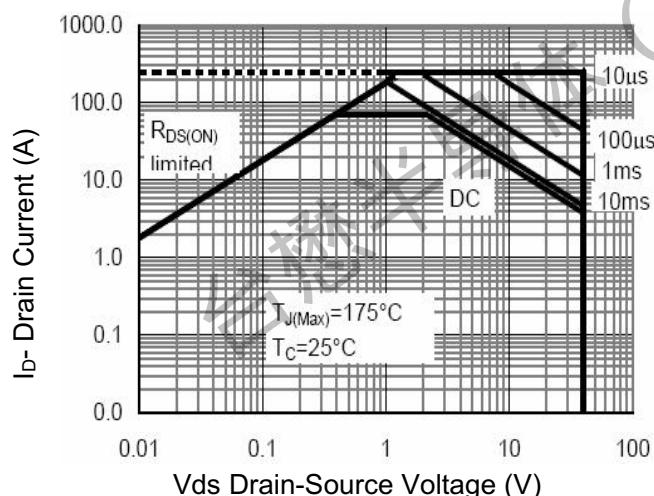


Figure 8 Safe Operation Area

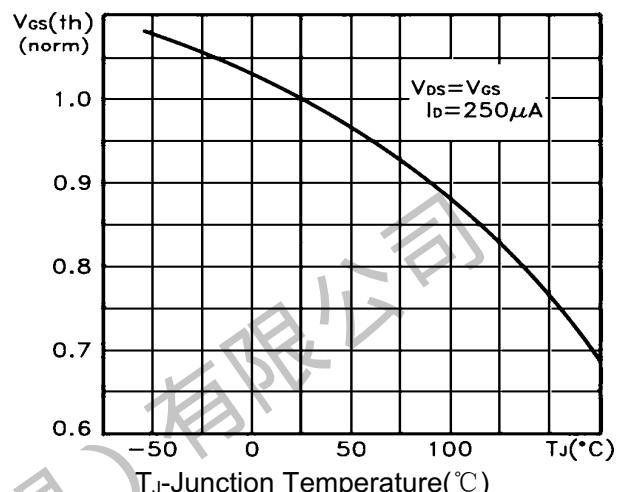


Figure 10 $V_{GS(th)}$ vs Junction Temperature

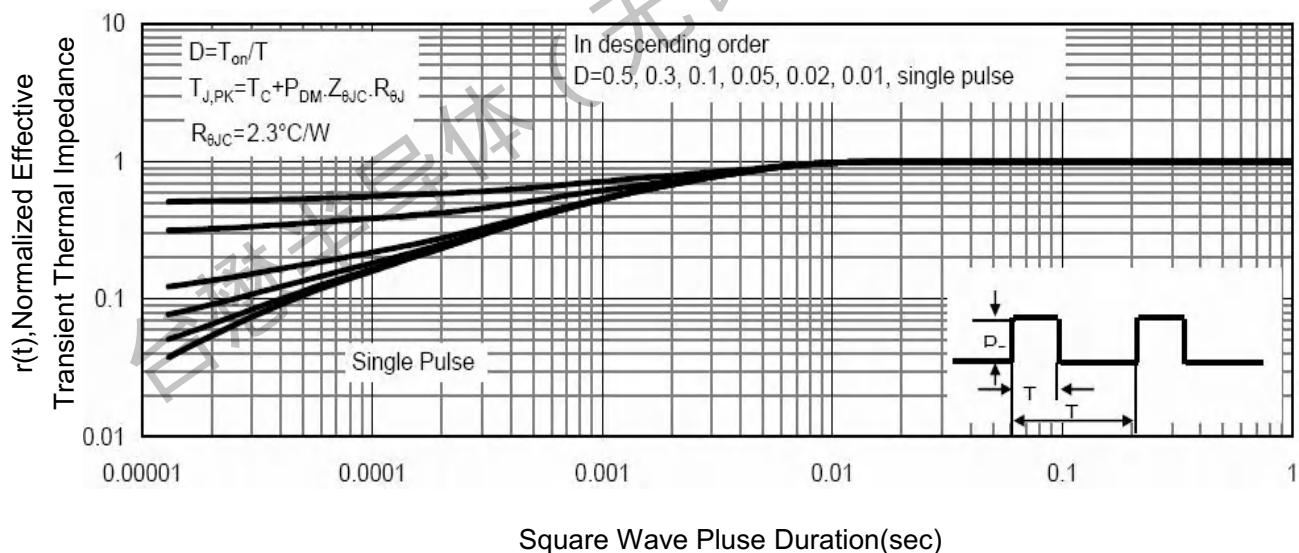
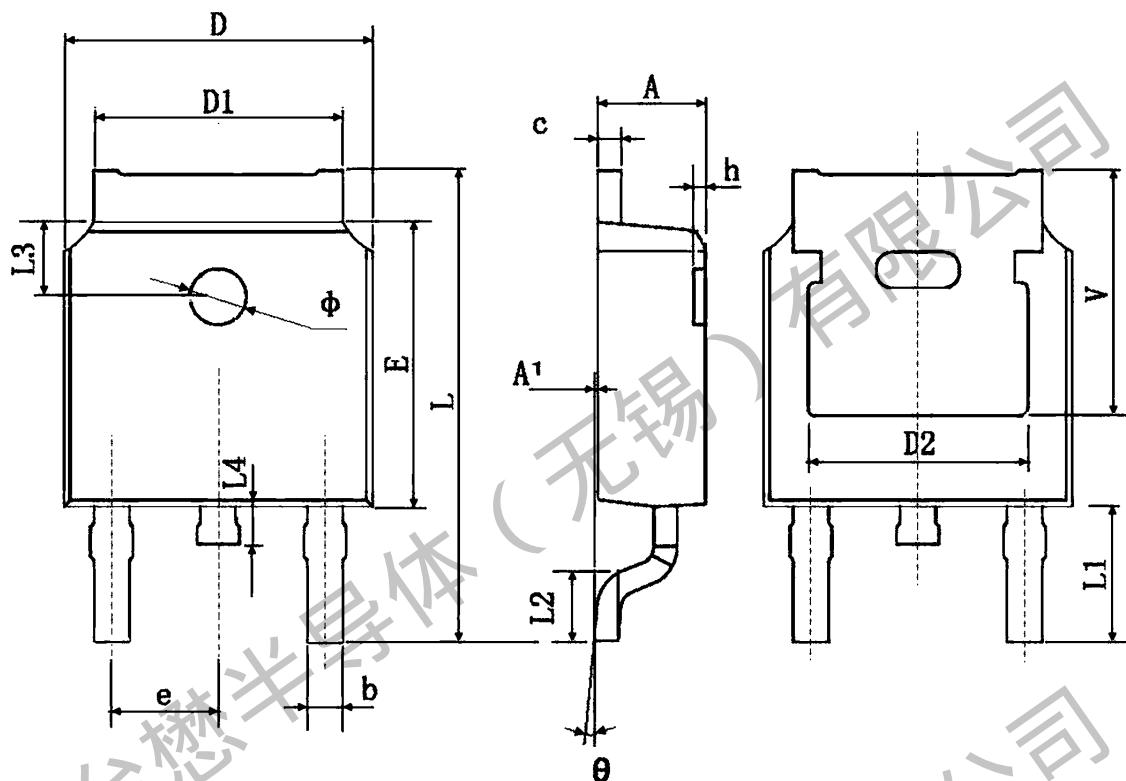


Figure 11 Normalized Maximum Transient Thermal Impedance

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Package Mechanical Data: TO-252-3L

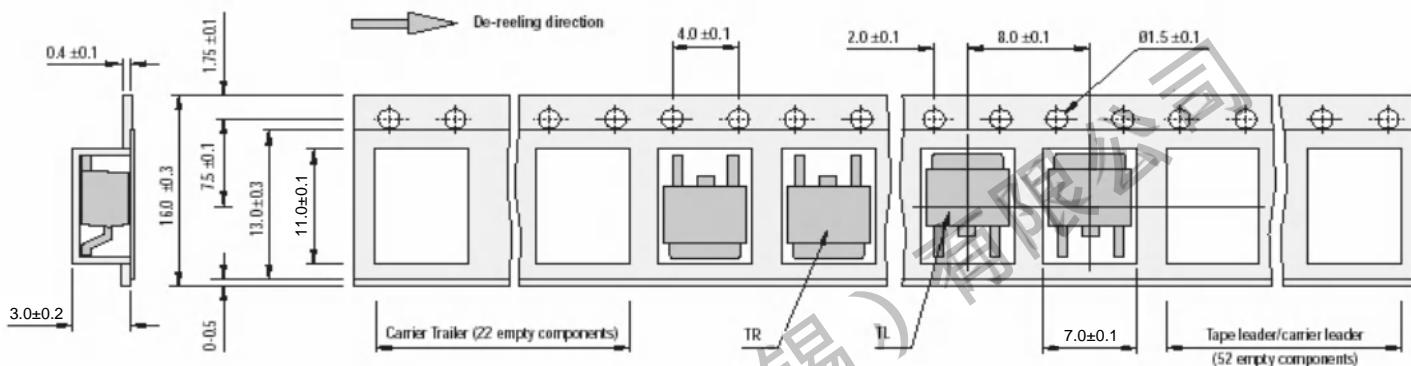


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830 TYP.		0.190 TYP.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 TYP.		0.114 TYP.	
L2	1.400	1.700	0.055	0.067
L3	1.600 TYP.		0.063 TYP.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350 TYP.		0.211 TYP.	

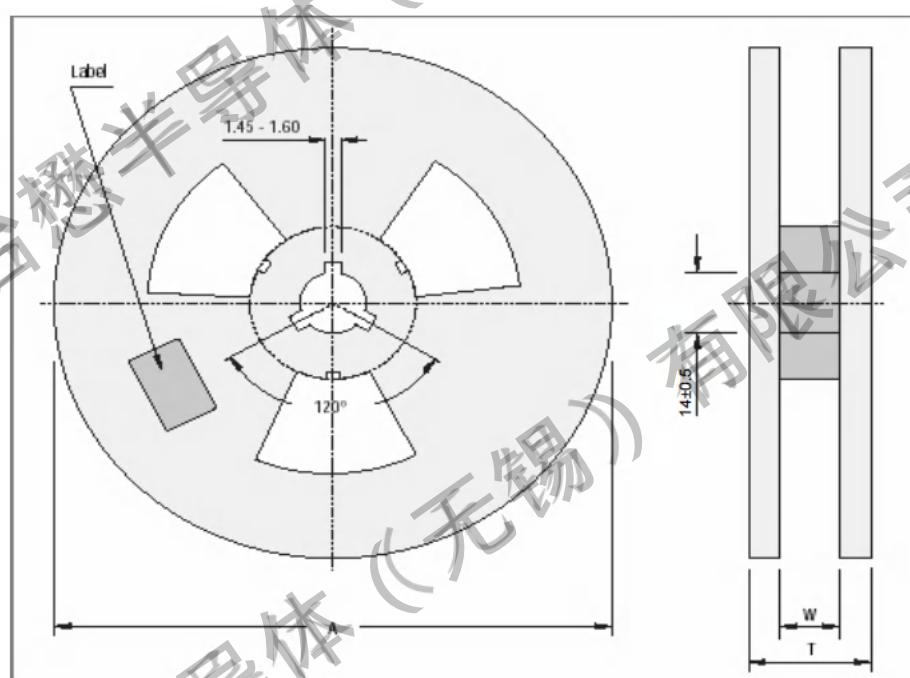
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TO-252-3L Embossed Carrier Tape



TO-252-3L Reel



All Dimensions are in mm.

Reel Specifications

Package	Tape Width	Reel Dia. A - Max	Inside Thickness W	Reel Thickness T - max
TO-252-3L	16	330	18.0 ± 1.5	20

Packaging Information

REEL	Reel Size	Box	Box Size(mm)	Carton	Carton Size(mm)	G.W.(kg)
2,500 pcs	13 inch	5,000 pcs	355×370×50	25,000 pcs	380×275×380	

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Revision history:

Date	Rev	Description	Page
2023.04.19	23.04	Original	