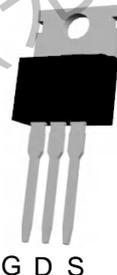
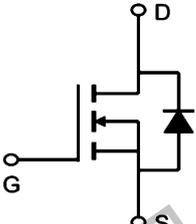


TM100N04P

N-Channel Enhancement Mosfet

<p><b>General Description</b></p> <ul style="list-style-type: none"> <li>• Low <math>R_{DS(ON)}</math></li> <li>• RoHS and Halogen-Free Compliant</li> </ul> <p><b>Applications</b></p> <ul style="list-style-type: none"> <li>• Load switch</li> <li>• PWM</li> </ul>	<p><b>General Features</b></p> <p><math>V_{DS} = 40V</math> <math>I_D = 100A</math>  <math>R_{DS(ON)} = 3.8m\Omega(\text{typ.}) @ V_{GS}=10V</math></p> <p>100% UIS Tested          100% <math>R_g</math> Tested</p> 
--	--

P:TO-220AB

Marking: 100N04

G D S

**Absolute Maximum Ratings:** ( $T_C=25^\circ C$  unless otherwise noted)

Symbol	Parameter	Ratings	Units
$V_{DS}$	Drain-Source Voltage	40	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current <sup>1</sup>	100	A
	Continuous Drain Current- $T_C=100^\circ C$	65	
	Pulsed Drain Current <sup>2</sup>	370	
$E_{AS}$	Single Pulse Avalanche Energy <sup>3</sup>	1080	mJ
$P_D$	Power Dissipation <sup>4</sup>	130	W
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55 to +175	$^\circ C$

**Thermal Characteristics:**

Symbol	Parameter	Max	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case <sup>1</sup>	1.15	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient <sup>1</sup>	---	

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Electrical Characteristics: ( $T_C=25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	40	45	---	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{GS}=0V, V_{DS}=40V$	---	---	1	$\mu A$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0A$	---	---	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(th)}$	GATE-Source Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1.2	1.6	2.0	V
$R_{DS(on)}$	Drain-Source On Resistance <sup>2</sup>	$V_{GS}=10V, I_D=20A$	---	3.8	5.9	m $\Omega$
		$V_{GS}=4.5V, I_D=10A$	---	5.8	7	
$G_{FS}$	Forward Transconductance	$V_{DS}=10V, I_D=20A$	26	---	---	S
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=20V, V_{GS}=0V, f=1MHz$	---	4400	---	pF
$C_{oss}$	Output Capacitance		---	970	---	
$C_{rss}$	Reverse Transfer Capacitance		---	380	---	
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-On Delay Time	$V_{DD}=20V, I_D=2A,$ $R_{GEN}=3\Omega, V_{GS}=10V$	---	15	---	ns
$t_r$	Rise Time		---	18	---	ns
$t_{d(off)}$	Turn-Off Delay Time		---	52	---	ns
$t_f$	Fall Time		---	23	---	ns
$Q_g$	Total Gate Charge	$V_{GS}=10V, V_{DS}=20V,$ $I_D=20A$	---	75	---	nC
$Q_{gs}$	Gate-Source Charge		---	10.5	---	nC
$Q_{gd}$	Gate-Drain "Miller" Charge		---	17	---	nC
<b>Drain-Source Diode Characteristics</b>						
$V_{SD}$	Source-Drain Diode Forward Voltage <sup>2</sup>	$V_{GS}=0V, I_S=40A$	---	---	1.2	V
$I_S$	Diode Forward Current (Note 2)	---	---	---	100	A
$T_{rr}$	Reverse Recovery Time	$T_J = 25^\circ\text{C}, I_F = 40A$ $di/dt = 100A/\mu s$ <sup>(Note3)</sup>	---	42	---	NS
$Q_{rr}$	Reverse Recovery Charge		---	45	---	NC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production
5.  $E_{AS}$  condition :  $T_J=25^\circ\text{C}, V_{DD}=20V, V_G=10V, L=1mH, R_G=25\Omega, I_{AS}=46.5A$

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Typical Characteristics

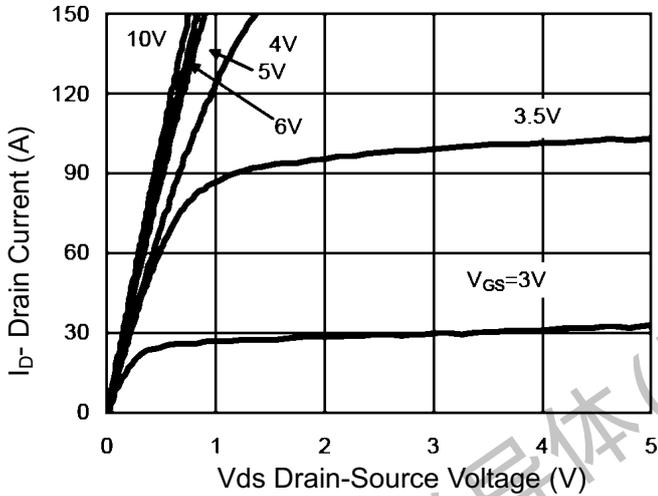


Figure 1 Output Characteristics

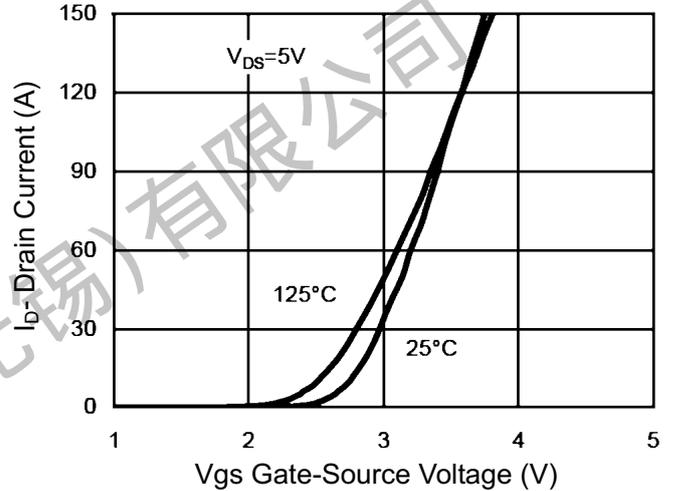


Figure 2 Transfer Characteristics

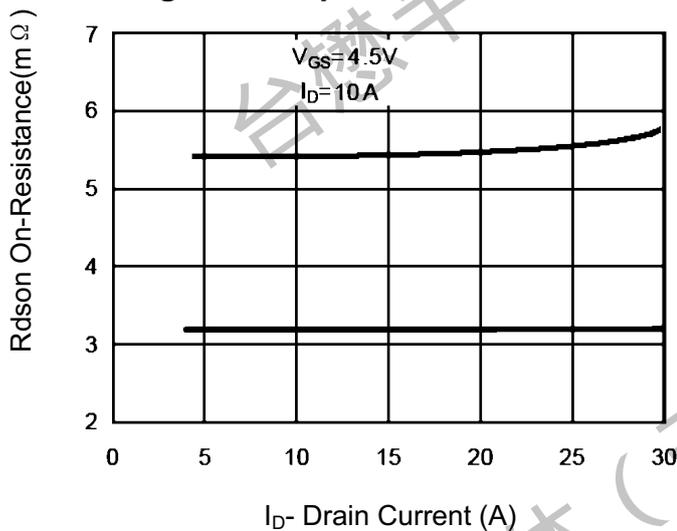


Figure 3 Rdson- Drain Current

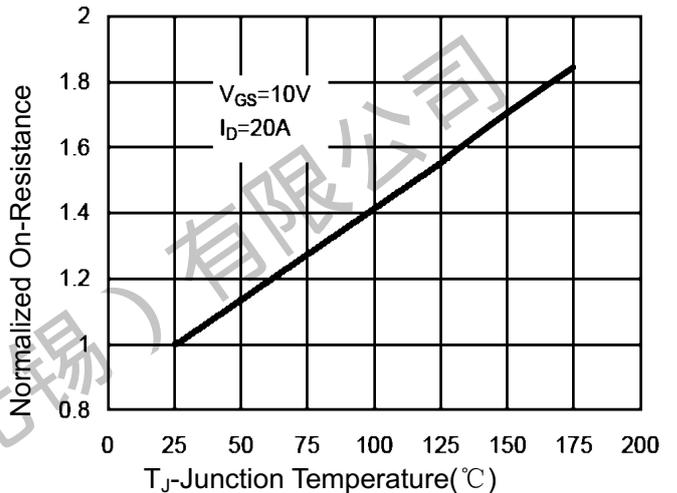


Figure 4 Rdson-Junction Temperature

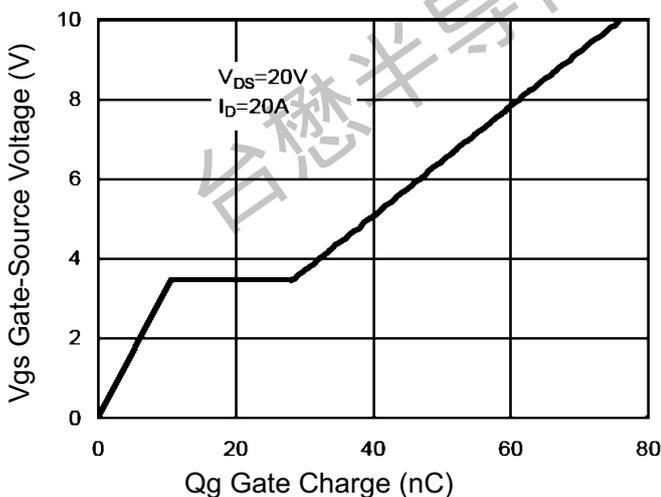


Figure 5 Gate Charge

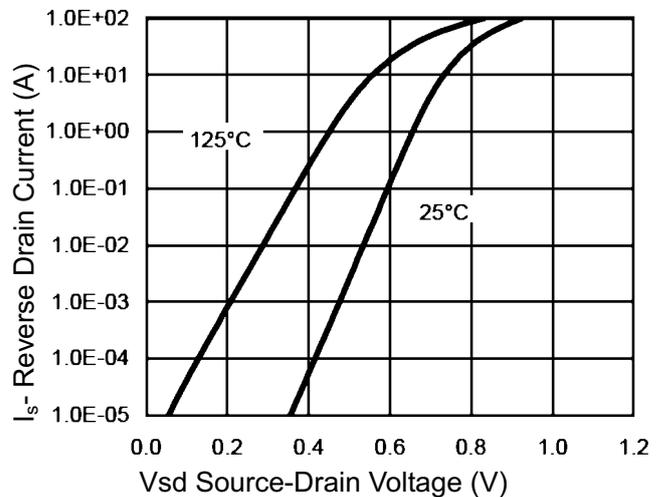


Figure 6 Source- Drain Diode Forward

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N-Channel Enhancement Mosfet

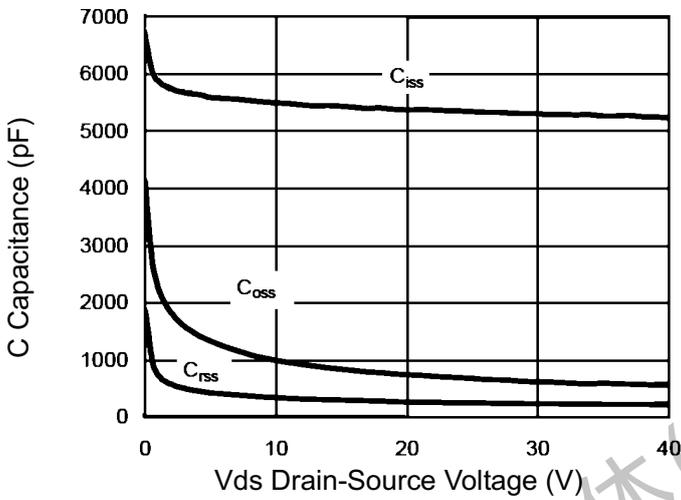


Figure 7 Capacitance vs Vds

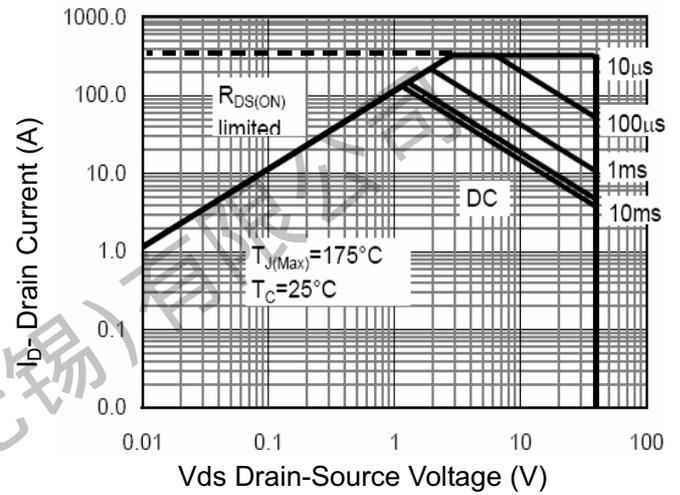


Figure 8 Safe Operation Area

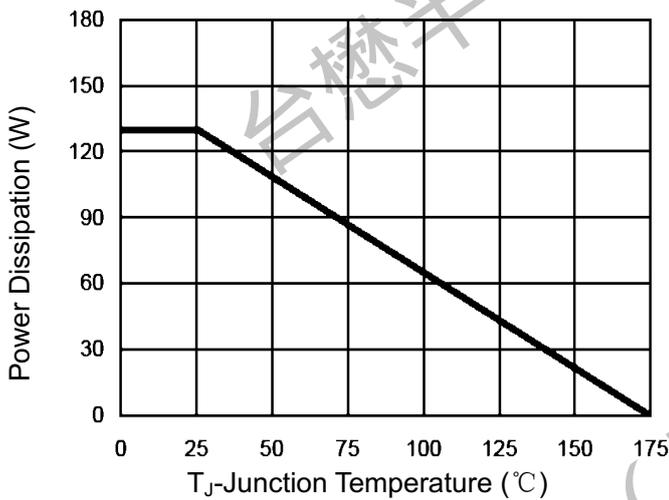


Figure 9 Power De-rating

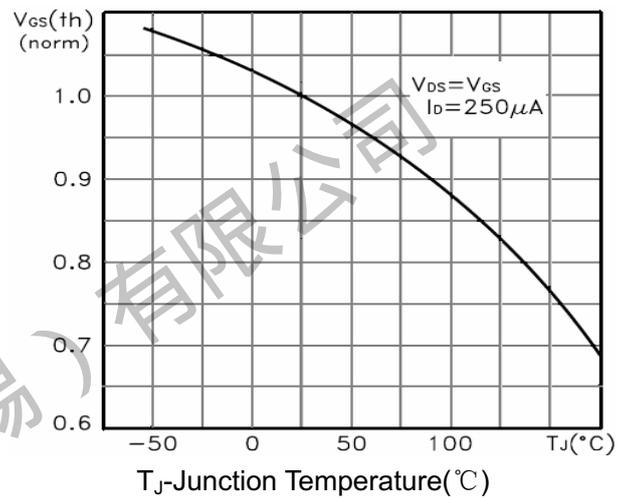


Figure 10  $V_{GS(th)}$  vs Junction Temperature

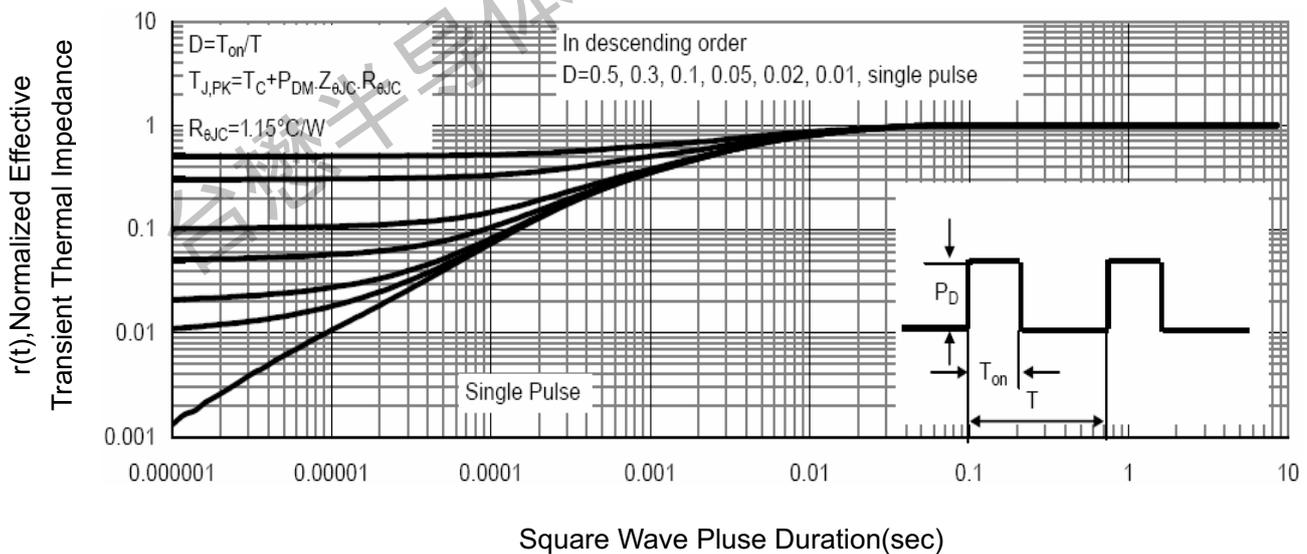
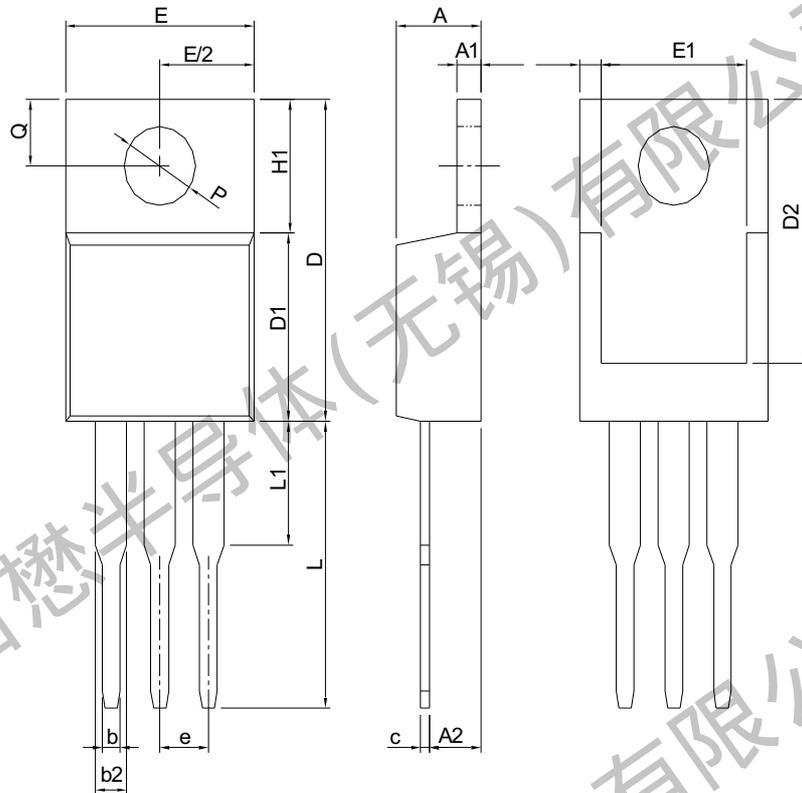


Figure 11 Normalized Maximum Transient Thermal Impedance

TM100N04P

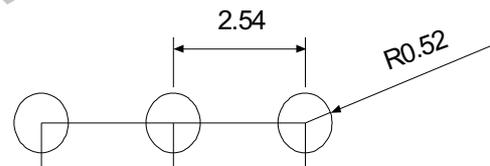
N-Channel Enhancement Mosfet

Package Mechanical Data: TO-220AB



DIMENSIONS	TO-220			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	3.56	4.83	0.140	0.190
A1	0.51	1.40	0.020	0.055
A2	2.03	2.92	0.080	0.115
b	0.38	1.02	0.015	0.040
b2	1.14	1.78	0.045	0.070
c	0.36	0.61	0.014	0.024
D	14.22	16.51	0.560	0.650
D1	8.38	9.02	0.330	0.355
D2	12.19	13.65	0.480	0.537
E	9.65	10.67	0.380	0.420
E1	6.86	8.89	0.270	0.350
e	2.54 BSC		0.100 BSC	
H1	5.84	6.86	0.230	0.270
L	12.70	14.73	0.500	0.580
L1	-	6.35	-	0.250
P	3.53	4.09	0.139	0.161
Q	2.54	3.43	0.100	0.135

RECOMMENDED LAND PATTERN

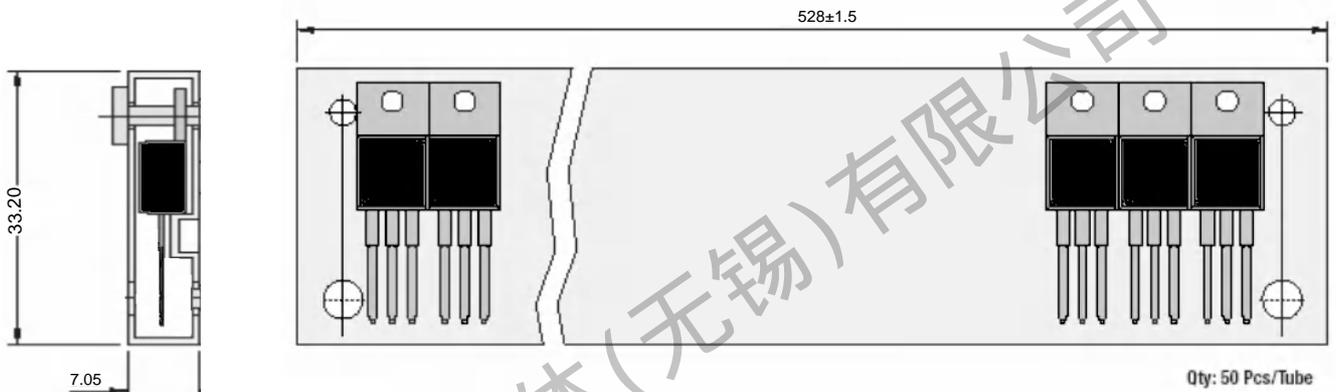


UNIT: mm

Note: Follow JEDEC TO-220 AB.

**TM100N04P**

**N-Channel Enhancement Mosfet**



All Dimensions are in mm

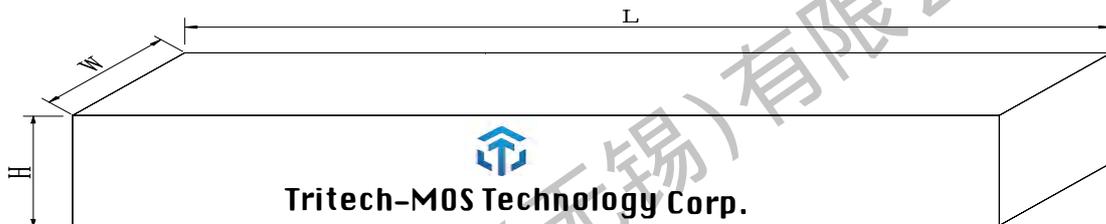
**1.TO-220AB Packaging**

Package	Packing Form	Quantity		
		Tube	Inner Box [kpcs]	Outbox [kpcs]
TO-220AB	Tube Tape	50	5	1

TM100N04P

N-Channel Enhancement Mosfet

Inner Box



Dimension : 580 (L)×154(W) ×49(H) mm

Quantity : 50 ×20Ea = 1Kpcs

Outer Box



Dimension : 595(L)×285(W) ×185(H) mm

Quantity : 1K×5Ea = 5Kpcs

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Revision history:

Date	Rev	Description	Page
2023.03.29	23.03	Original	