
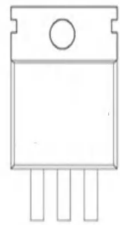

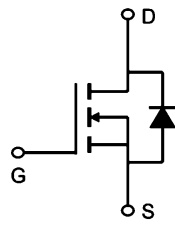


TM100N03P

N-Channel Enhancement Mosfet

<p>General Description</p> <ul style="list-style-type: none"> • Low $R_{DS(ON)}$ • RoHS and Halogen-Free Compliant <p>Applications</p> <ul style="list-style-type: none"> • Load switch • PWM 	<p>General Features</p> <p>$V_{DS} = 30V$ $I_D = 100A$ $R_{DS(ON)} = 3.6 m\Omega (typ.) @ V_{GS} = 10V$</p> <p>100% UIS Tested 100% R_g Tested</p> 
--	---

P:TO-220AB

Marking: 100N03 G D S

Absolute Maximum Ratings: ($T_C = 25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current ¹	100	A
	Continuous Drain Current- $T_C = 100^\circ C$ ¹	59	
I_{DM}	Pulsed Drain Current ²	360	
P_D	Power Dissipation	90	W
E_{AS}	Single pulse avalanche energy ³	250	mJ
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55-+175	$^\circ C$

Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case	15	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	62	$^\circ C/W$

TM100N03P

N-Channel Enhancement Mosfet

Electrical Characteristics: ($T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\ \mu\text{A}$	30	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS}=0V, V_{DS}=24V$	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0A$	---	---	± 100	nA
On Characteristics						
$V_{GS(th)}$	GATE-Source Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\ \mu\text{A}$	0.9	1.2	1.5	V
$R_{DS(on)}$	Drain-Source On Resistance ²	$V_{GS}=10V, I_D=40A$	---	3.6	4.8	m Ω
		$V_{GS}=4.5V, I_D=30A$	---	4.9	6.5	
G_{FS}	Forward Transconductance	$V_{DS}=10V, I_D=15A$	---	28	---	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=15V, V_{GS}=0V, f=1\text{MHz}$	---	1950	2350	pF
C_{oss}	Output Capacitance		---	320	---	
C_{rss}	Reverse Transfer Capacitance		---	240	---	
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	$V_{DD}=15V, I_D=15A,$ $V_{GS}=10V, R_{GEN}=3.3\Omega$	---	13	---	ns
t_r	Rise Time		---	36	---	ns
$t_{d(off)}$	Turn-Off Delay Time		---	43	---	ns
t_f	Fall Time		---	16	---	ns
Q_g	Total Gate Charge		---	42	84	nC
Q_{gs}	Gate-Source Charge	$V_{GS}=10V, V_{DS}=24V,$ $I_D=20A$	---	3.9	---	nC
Q_{gd}	Gate-Drain "Miller" Charge		---	14	---	nC
Drain-Source Diode Characteristics						
V_{SD}	Source-Drain Diode Forward Voltage ²	$V_{GS}=0V, I_S=30A$	---	---	1.2	V
t_{rr}	Reverse Recovery Time	$I_S=10A, V_{GS}=0V,$ $di/dt=100A$		16	---	Ns
q_{rr}	Reverse Recovery Charge			5	---	nc

Notes:

- 1.Pulse width limited by max. junction temperature
- 2.Pulse test
- 3.Surface mounted on 1 in² copper pad of FR4 board

TM100N03P

N-Channel Enhancement Mosfet

Typical Characteristics

Figure 1: Output Characteristics

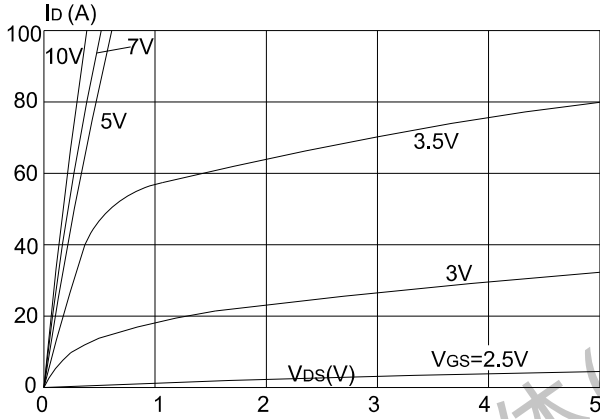


Figure 2: Typical Transfer Characteristics

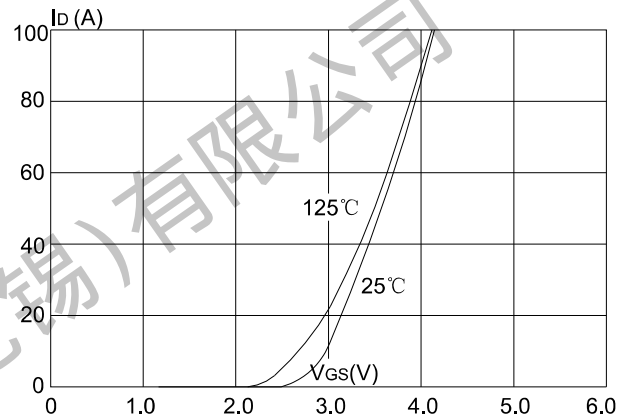


Figure 3: On-resistance vs. Drain Current

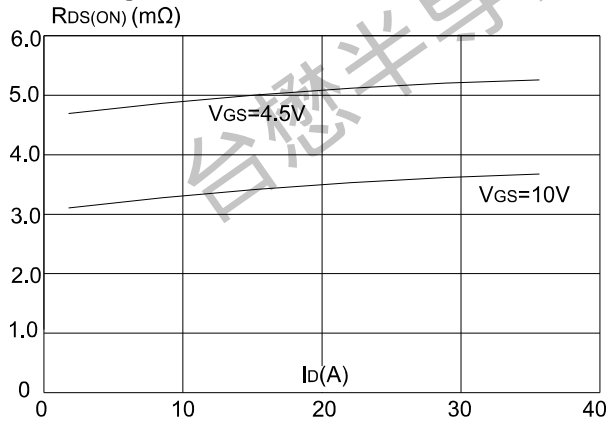


Figure 4: Body Diode Characteristics

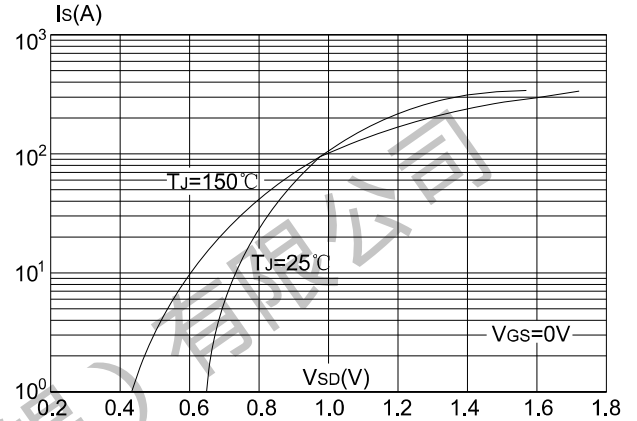


Figure 5: Gate Charge Characteristics

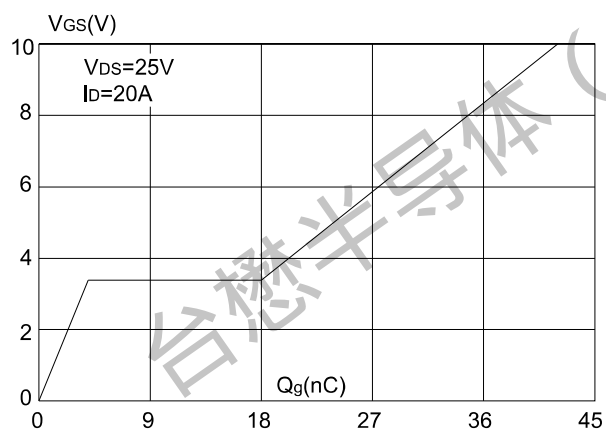
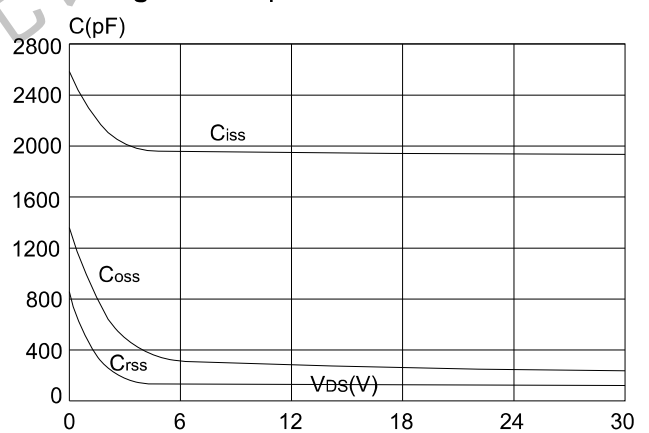


Figure 6: Capacitance Characteristics



TM100N03P

N-Channel Enhancement Mosfet

Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

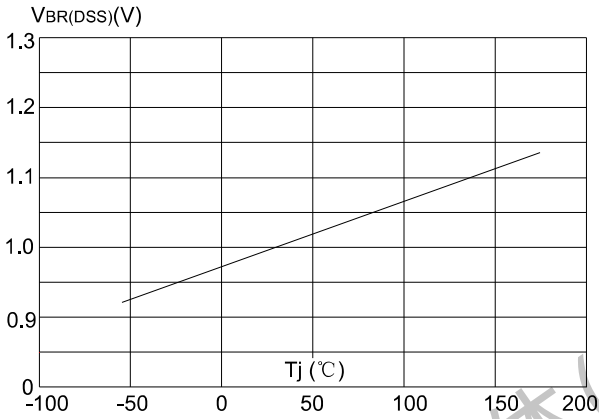


Figure 8: Normalized on Resistance vs. Junction Temperature

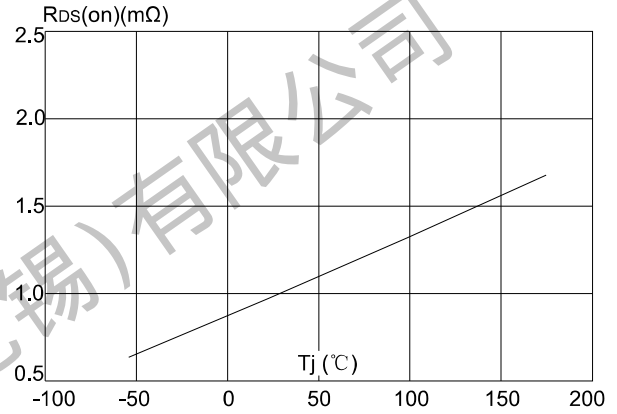


Figure 9: Maximum Safe Operating Area

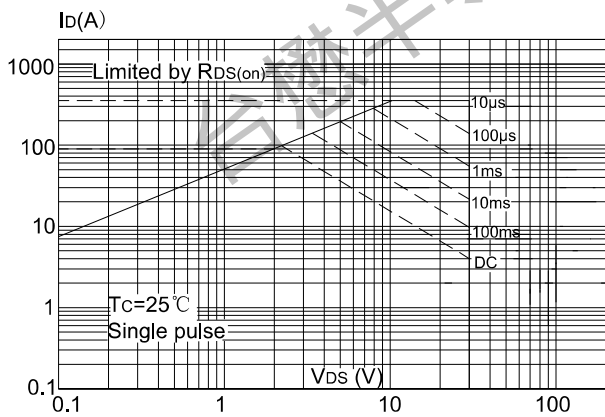


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

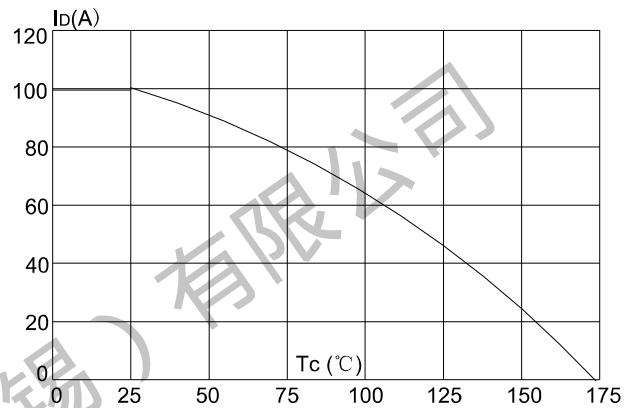
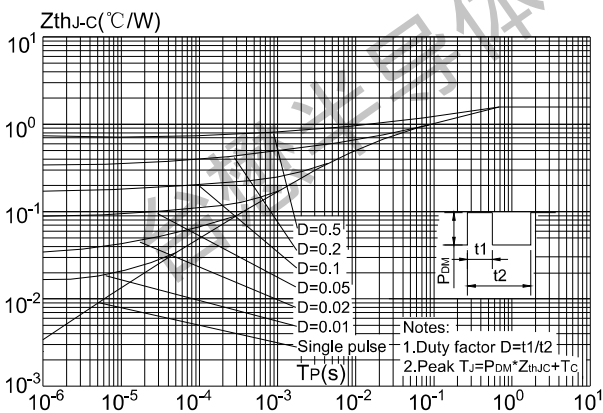


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case (TO-252)

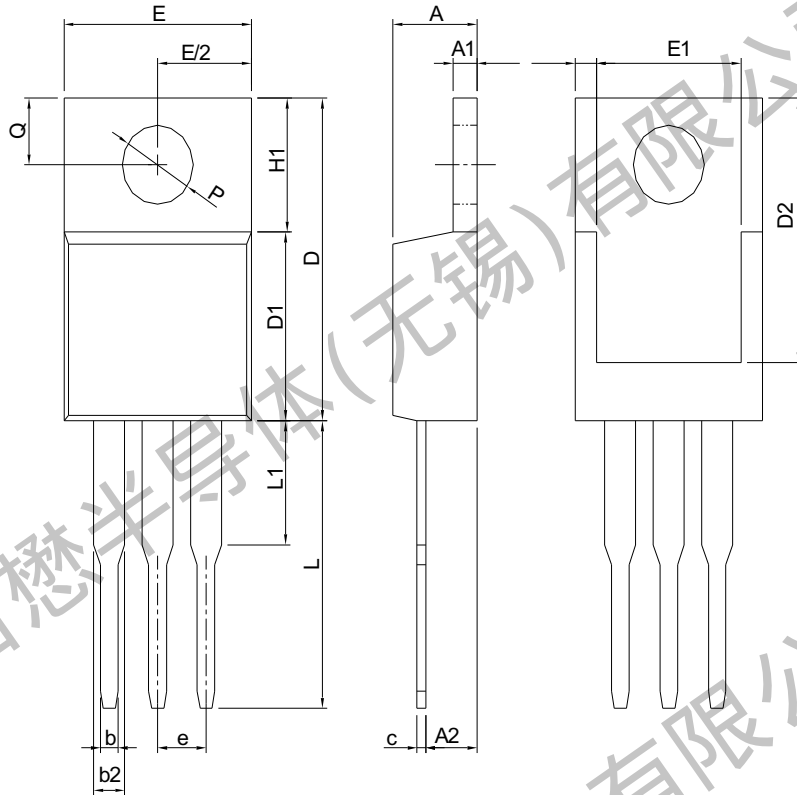




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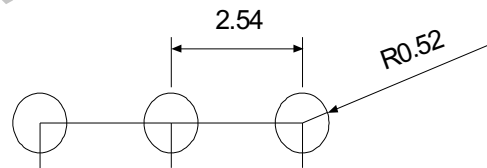
N-Channel Enhancement Mosfet

Package Mechanical Data: TO-220AB



DIMENSIONS	TO-220			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	3.56	4.83	0.140	0.190
A1	0.51	1.40	0.020	0.055
A2	2.03	2.92	0.080	0.115
b	0.38	1.02	0.015	0.040
b2	1.14	1.78	0.045	0.070
c	0.36	0.61	0.014	0.024
D	14.22	16.51	0.560	0.650
D1	8.38	9.02	0.330	0.355
D2	12.19	13.65	0.480	0.537
E	9.65	10.67	0.380	0.420
E1	6.86	8.89	0.270	0.350
e	2.54 BSC		0.100 BSC	
H1	5.84	6.86	0.230	0.270
L	12.70	14.73	0.500	0.580
L1	-	6.35	-	0.250
P	3.53	4.09	0.139	0.161
Q	2.54	3.43	0.100	0.135

RECOMMENDED LAND PATTERN



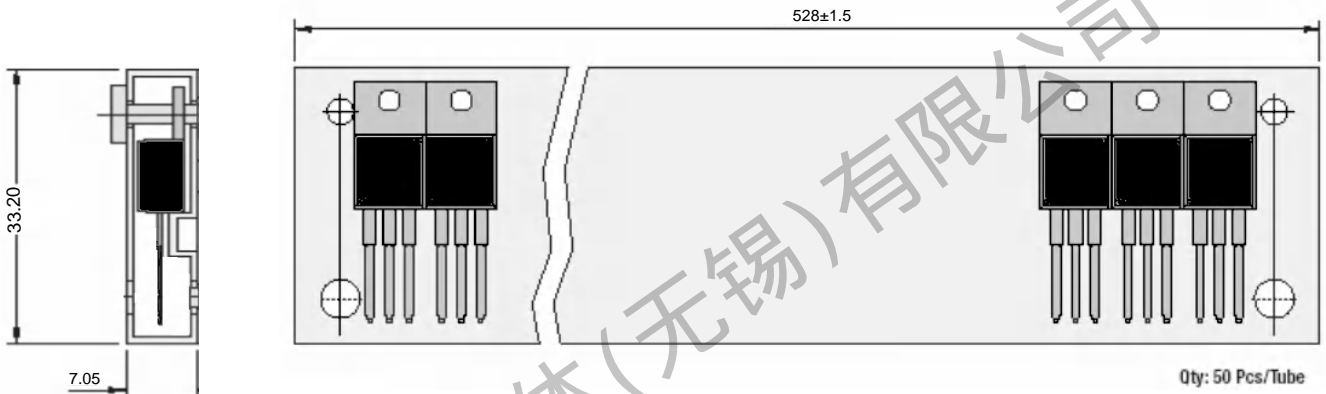
UNIT: mm

Note: Follow JEDEC TO-220 AB.



TM100N03P

N-Channel Enhancement Mosfet



All Dimensions are in mm

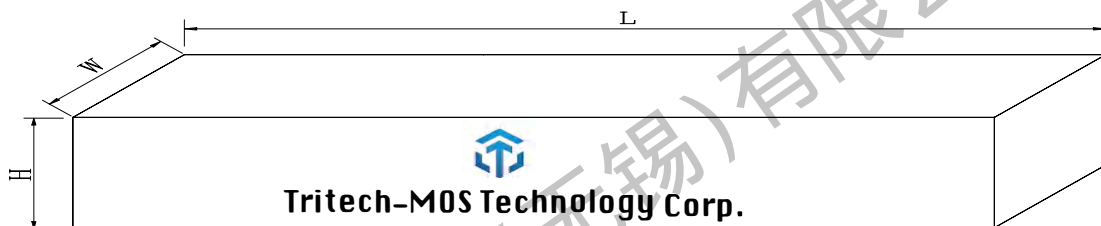
1.TO-220AB Packaging

Package	Packing Form	Quantity		
		Tube	Inner Box [kpcs]	Outbox [kpcs]
TO-220AB	Tube Tape	50	5	1

TM100N03P

N-Channel Enhancement Mosfet

Inner Box



Dimension : 580 (L)×154(W) ×49(H) mm

Quantity : 50 ×20Ea = 1Kpcs

Outer Box



Dimension : 595(L)×285(W) ×185(H) mm

Quantity : 1K×5Ea = 5Kpcs



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Revision history:

Date	Rev	Description	Page
2023.06.22	23.06	Original	